

Hourly Position Available in Reconfigurable Computing Lab

Project Title: Implementation of an Image Reconstruction Technique on FPGA

Objective:

- Implement a given algorithm and technique specifically designed to deal with the image reconstruction methods for Magnetic Resonance Imaging (MRI) in VHDL/Verilog
- Analyze computation characteristics of the algorithm, mold it for FPGA based implementation
- Build a CPU-FPGA integrated image reconstruction engine that will speed up the reconstruction process significantly.

Project Duration: Starting immediately till May 2008

Project Status: Ongoing, Some preliminary implementation has already been completed.

Required

Background: Excellent in VHDL for RTL Synthesis

Programming Skills: C, Matlab

For further information please contact Ali Akoglu (akogle@ece.arizona.edu) along with your resume.