

ECE 474A/574A Survey, Fall 2009

Name _____

Email _____

Major/Department _____

Year (circle one) *freshman* *sophomore* *junior* *senior* *graduate MS* *graduate PhD*

1. Have you taken any of the following courses (or an equivalent)?

<input type="checkbox"/>	ECE 175 Computer Programming for Engineering Applications (<i>Fundamentals of C, introduction to data structures</i>)
<input type="checkbox"/>	ECE 274 Digital Logic (<i>Number systems, logic design, sequential systems</i>)
<input type="checkbox"/>	ECE 369 Fundamentals of Computer Architecture (<i>Computer architecture and organization</i>)
<input type="checkbox"/>	ECE 372 Microprocessor Organization (<i>Computer organization and assembly language, memory devices, peripherals</i>)
<input type="checkbox"/>	ECE 373 Object-Oriented Software Design (<i>Object oriented computing concepts, classes, abstract data types, inheritance</i>)
<input type="checkbox"/>	ECE 407 Digital VLSI Systems Design (<i>Design, analysis, and layout of digital CMOS circuits and systems</i>)
<input type="checkbox"/>	ECE 462/562 Computer Architecture and Design (<i>Instruction set design, ALU design, memory organization and design, cache design</i>)
<input type="checkbox"/>	ECE 473 Software Engineering Concepts (<i>Phases of software, object-oriented design and programming</i>)
<input type="checkbox"/>	ECE 576 Engineering of Computer-Based Systems (<i>Design methodologies, object-oriented modeling, system synthesis</i>)
<input type="checkbox"/>	ECE 672 Computer-aided Design Algorithms and Techniques for VLSI (<i>VLSI design, synthesis, layout, optimization, simulation</i>)

2. How familiar are you with the following topics (1 – never hear of it, 5 – I am extremely comfortable with this topic)?

1	2	3	4	5	K-maps, 2-level AND/OR logic, truth tables
1	2	3	4	5	Boolean Algebra
1	2	3	4	5	Basic Datapath components – muxes, encoders, decoders
1	2	3	4	5	Storage Elements – flip-flops, latches, registers
1	2	3	4	5	State Machines – mealy and moore
1	2	3	4	5	RTL (Register-Transfer Level) Design
1	2	3	4	5	Hardware Description Languages - Verilog/VHDL
1	2	3	4	5	C/C++ Programming and data structures
1	2	3	4	5	CAD Tools – Xilinx or ModelSim

3. Anything else you want to tell me about your background?
